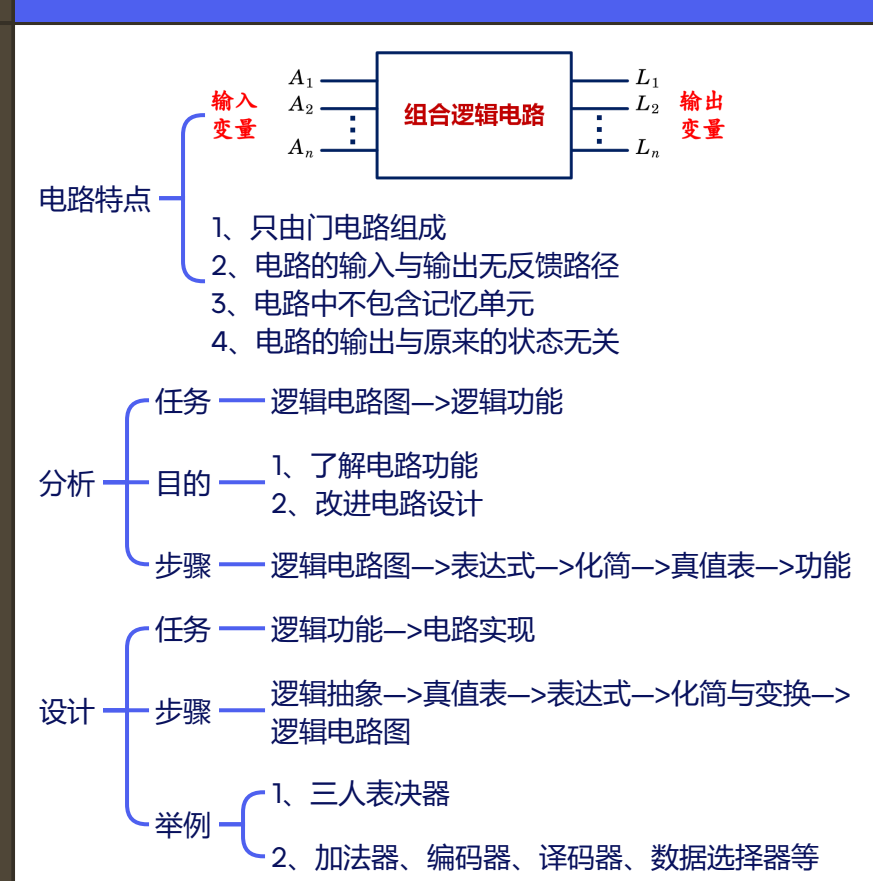
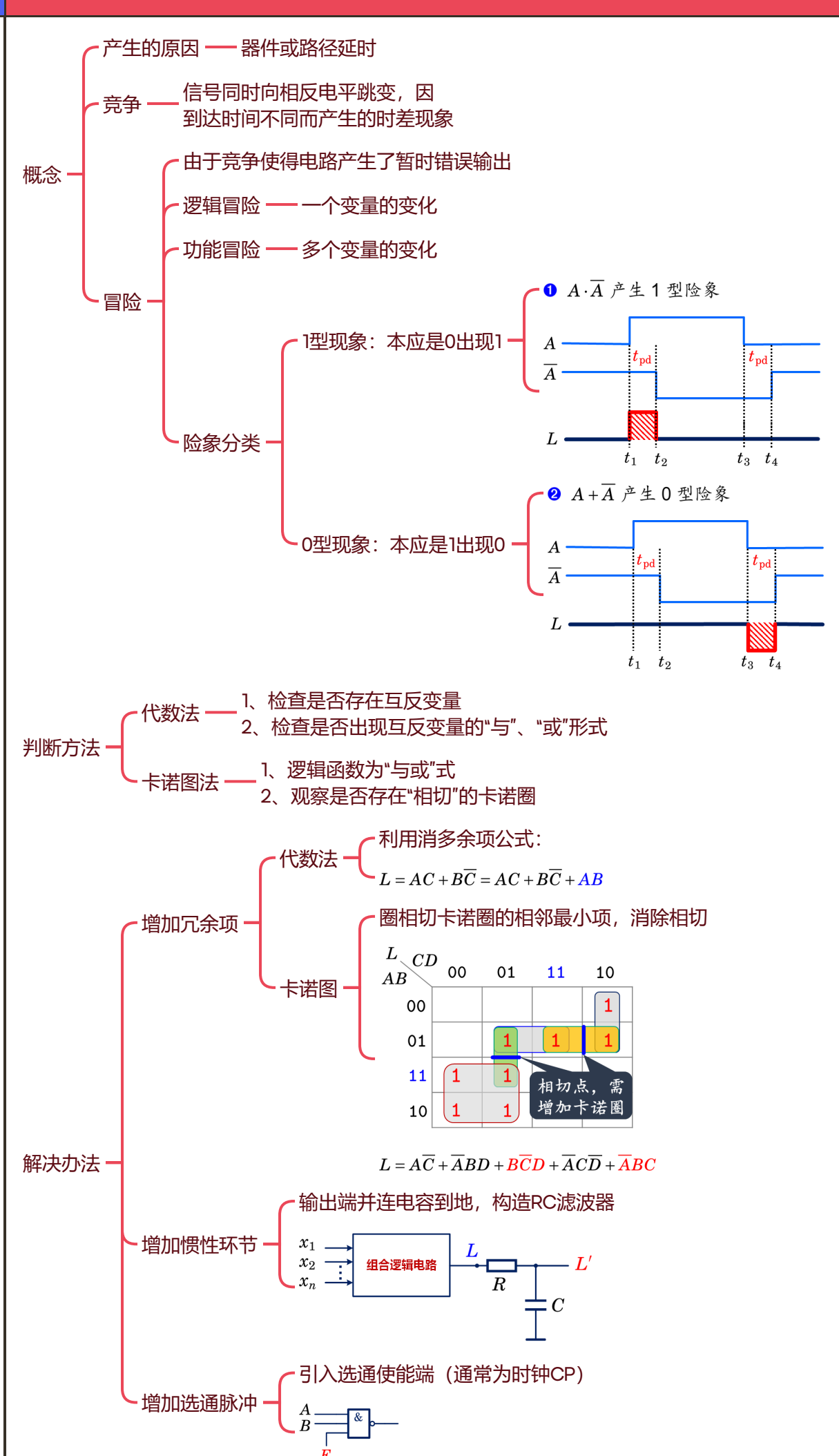


第三章 组合逻辑电路

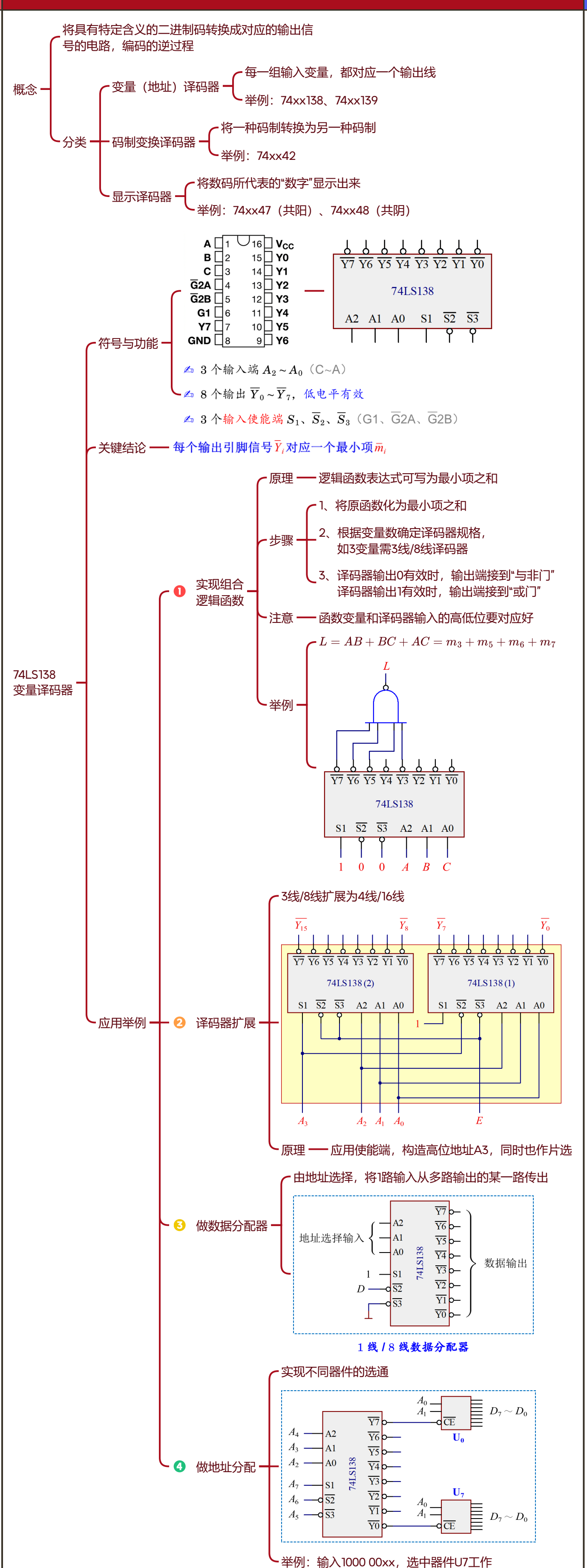
概念、分析与设计一般方法



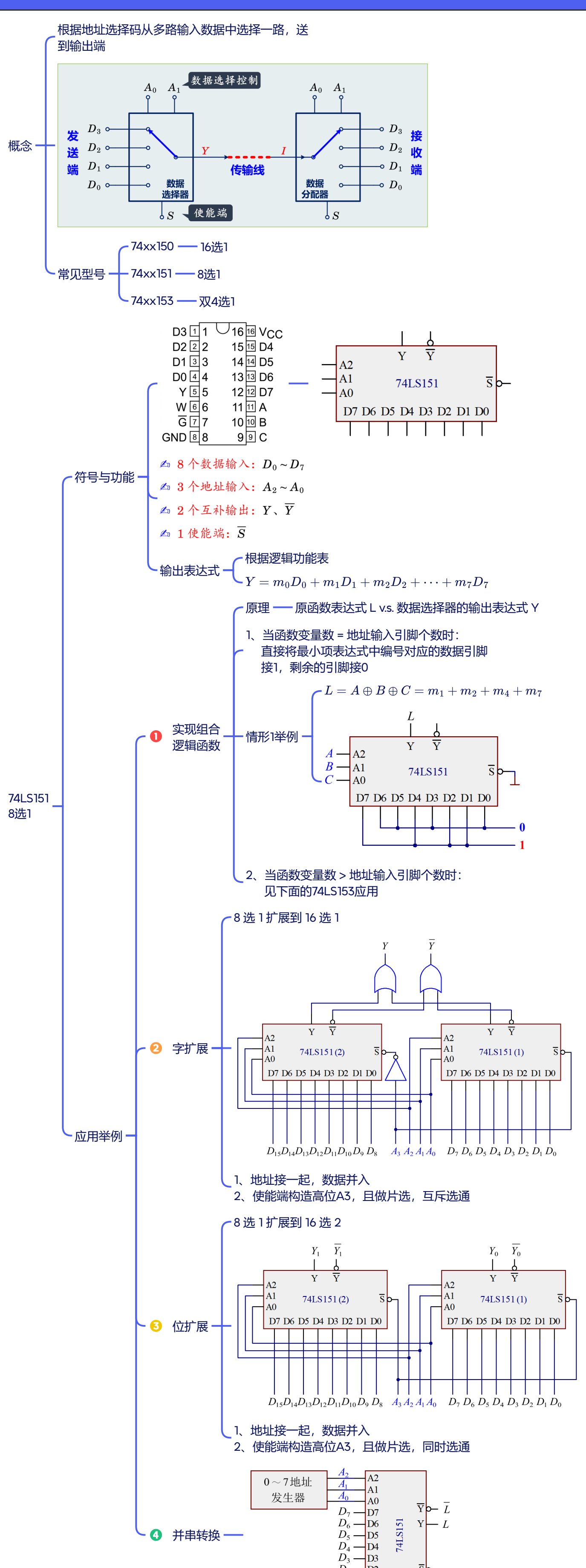
竞争与冒险



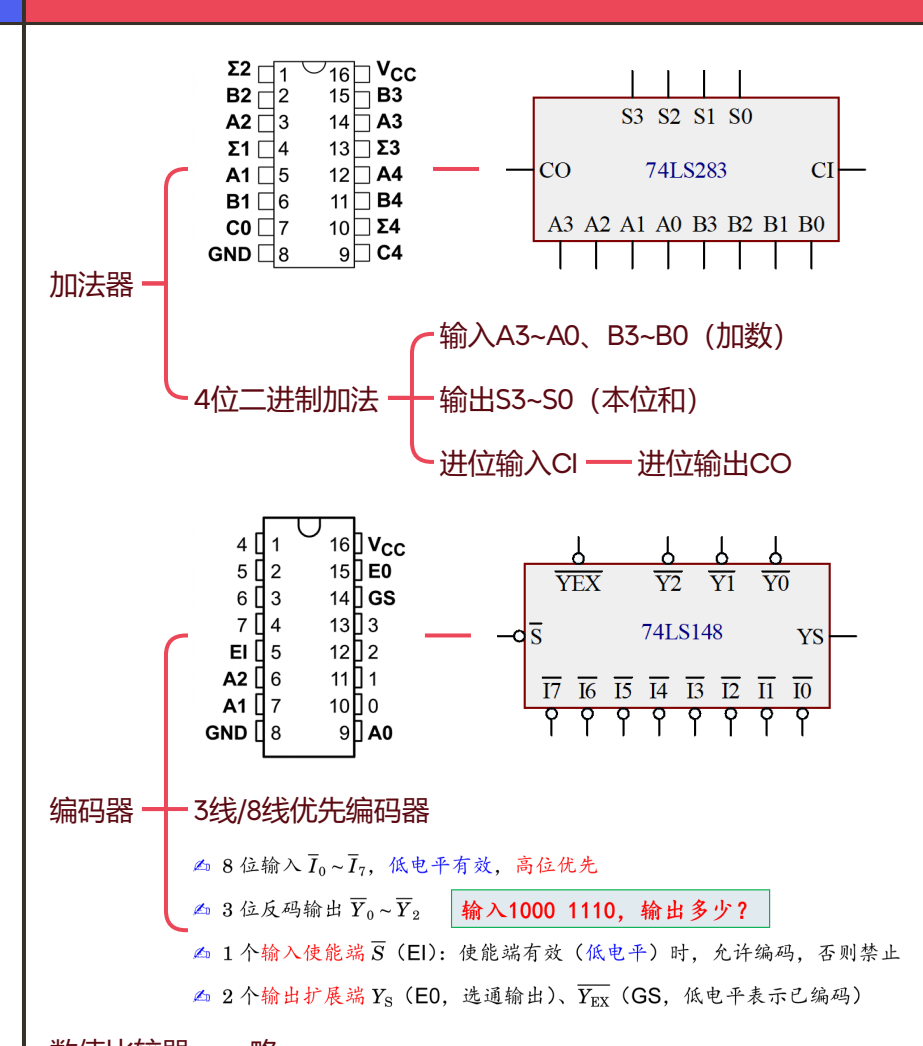
集成芯片-译码器



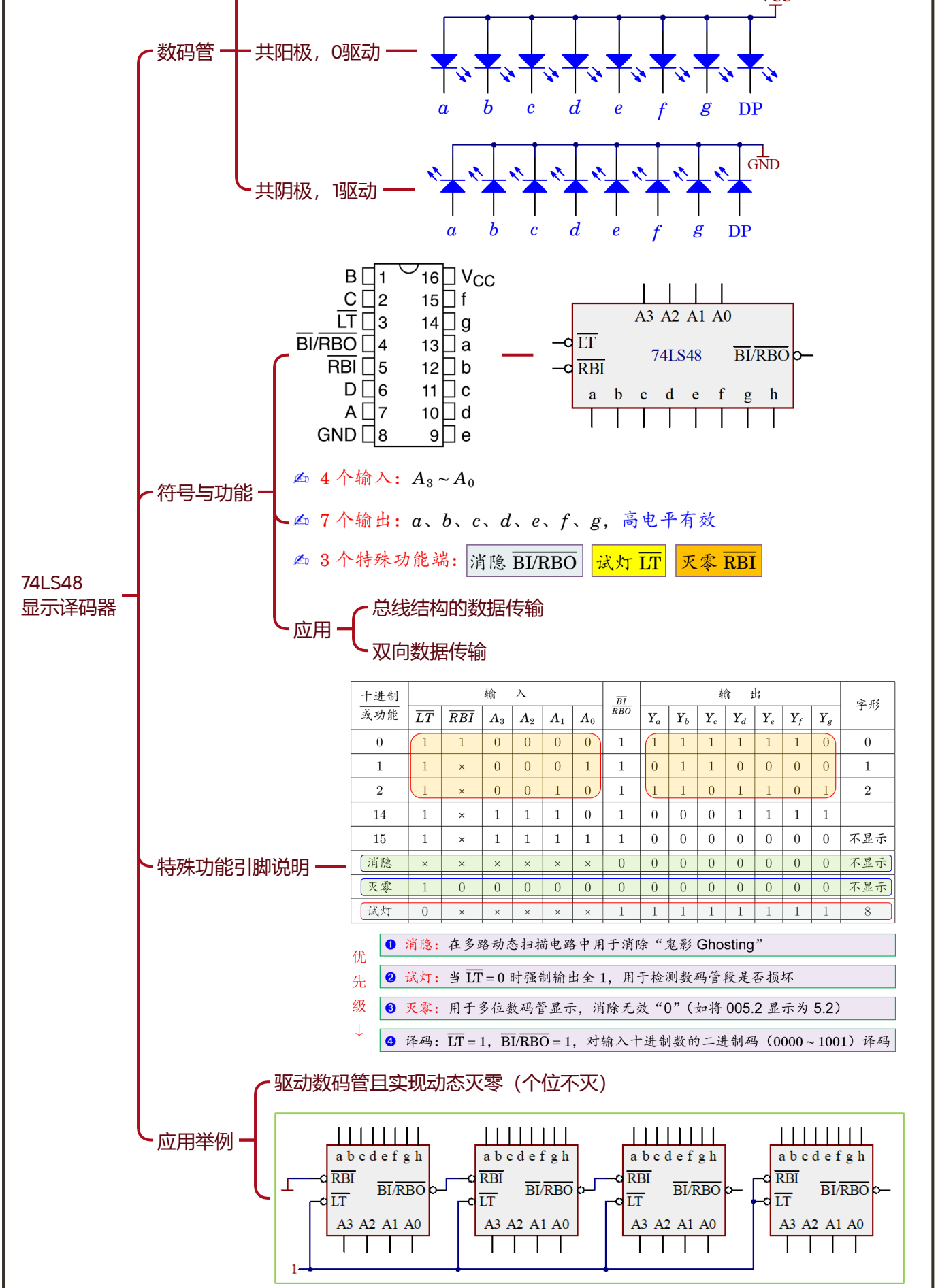
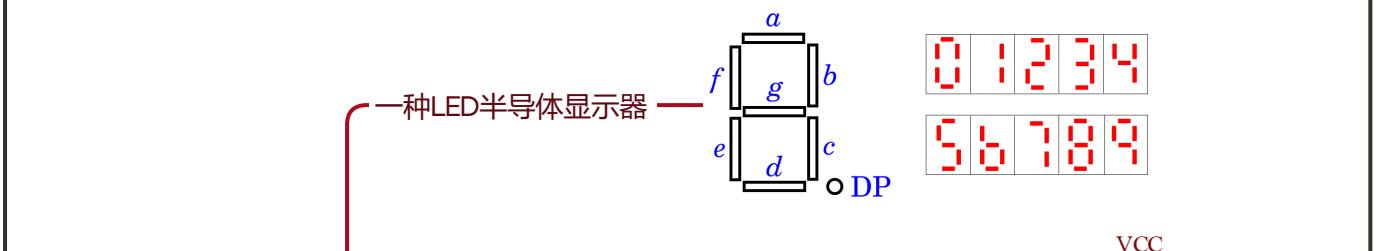
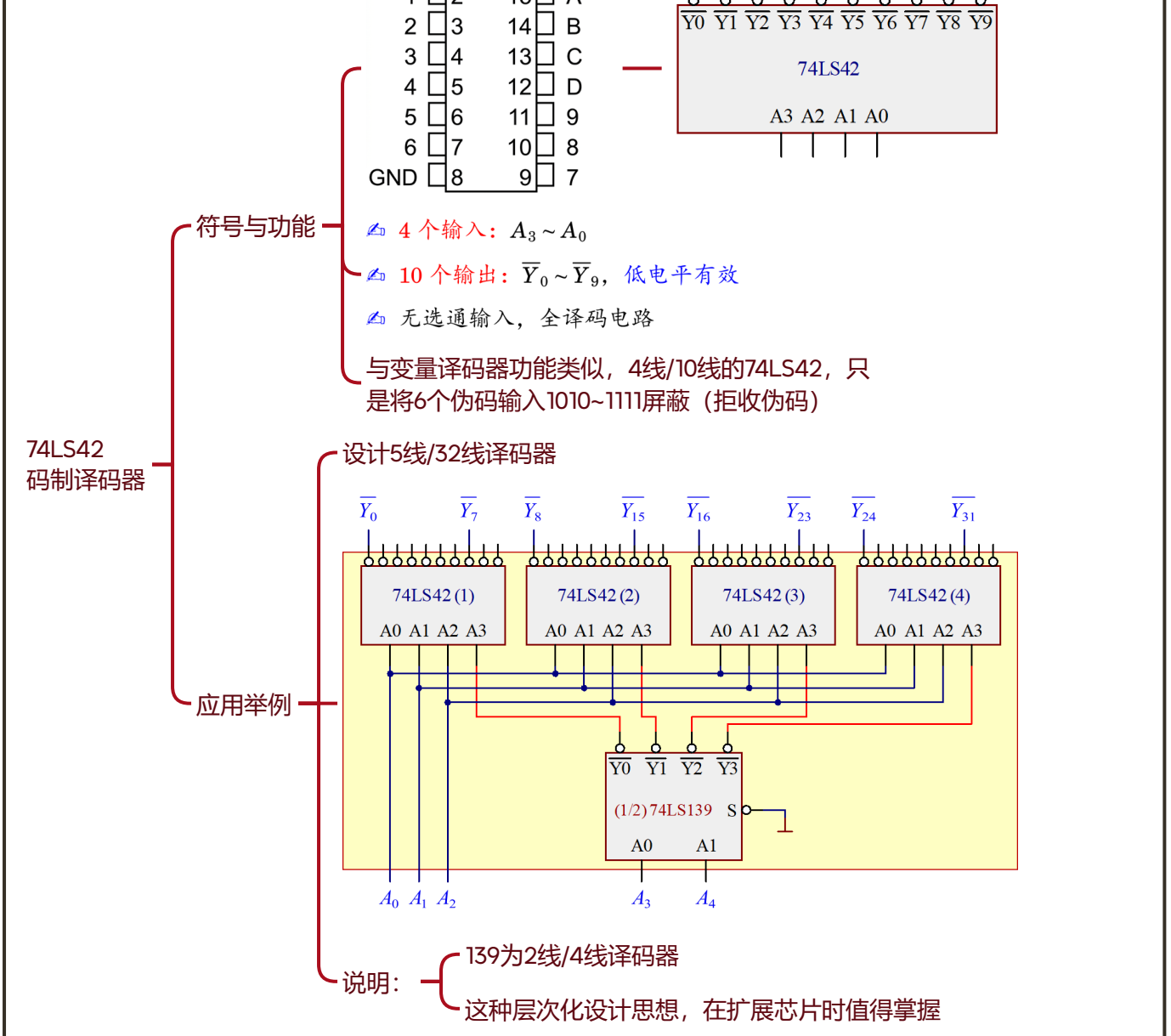
集成芯片-数据选择器



集成芯片-其他



74LS42 译码器



74LS153 双4选1

